

MUST Curriculum Planning for Graduate Students for Academic Year 2026,
Institute of Ph.D. Program in Semiconductor Technology

1 st year(115)					2 nd year(116)					3 rd year(117)								
	Course	1 st semester		2 nd semester			Course	1 st semester		2 nd semester			Course	1 st semester		2 nd semester		
		Cr.	hr.	Cr.	hr.			Cr.	hr.	Cr.	hr.			Cr.	hr.	Cr.	hr.	
MUST Core Required Courses						MUST Core Required Courses						MUST Core Required Courses	Doctoral Dissertation(I)/(II)	3		3		
	Subtotal	0	0	0	0		Subtotal	0	0	0	0		Subtotal	3	0	3	0	
School Professional Required Courses	Semiconductor Technology Seminar(1)/(11)	2	2	2	2	School Professional Required Courses						School Professional Required Courses						
	Subtotal	2	2	2	2		Subtotal	0	0	0	0		Subtotal	0	0	0	0	
compulsory courses	Semiconductor Engineering(1)/(11)	3	3	3	3	compulsory courses						compulsory courses	Internship of Practical Technology Research and Development(1)/(11)	2		2		
	Subtotal	3	3	3	3		Subtotal	0	0	0	0		Subtotal	2	0	2	0	
Elective Courses	Nanotechnology	3	3			Elective Courses	Semiconductor Process Integration	3	3			Elective Courses						
	Semiconductor Processings Technology	3	3				Wide Bandgap Semiconductor Measurement and analysis technology	3	3									
	Solid state Physics	3	3				Power Diodes Devices	3	3									
	Semiconductor Materials	3	3				Spectral Analysis	3	3									
	Solar Photovoltaic Technology	3	3				Special Topics on Semiconductor Processings			3	3							
	Integrated Circuit Testing	3	3				Semiconductor Processings Lab.			3	3							
	Semiconductor Packaging Technology	3	3				Optical Detection Technology			3	3							
	Patent Search and Writing	2	2				Thin Films Measurement Technology			2	2							
	Physics of Semiconductor Devices			3	3		Semiconductor Bio-Medical Chip			3	3							
	Characterization of Materials			3	3													
	Devices Processings technology and Reliability			3	3													
	Semiconductor Packaging and Testing Practice			3	3													
	Optoelectric Materials and Devices			3	3													
	Big Data and Statistical Analysis Practice			3	3													
	Special topics on Nano Materials and Fabrication			3	3													

4 th year(118)					
	Course	1 st semester		2 nd semester	
		Cr.	hr.	Cr.	hr.
MUST Core Required Courses	Doctoral Dissertation(III)/(IV)	3		3	
	Subtotal	3	0	3	0
School Professional Required Courses					
	Subtotal	0	0	0	0
compulsory courses	Internship of Practical Technology Research and Development(III)/(IV)	2		2	
	Subtotal	2	0	2	0
Elective Courses					

(Cr./hr.=Credit/hour)

Remarks:

1. Minimum Graduation Credits: 48 credits, including 30 required credits and 18 professional elective credits (at least 9 credits must be taken within the College of Semiconductor Engineering).
2. Students in this Ph.D. program must take one course related to English proficiency training from the master's program in the university (excluding professional courses), but it will not count into graduation credits.
3. During the third and the fourth years, students must take ""Corporate Practice R&D"" courses for a total of 8 credits in 4 semesters, Which are required to engage in research and development work at the related industry or corporate institution in order to complete ones thesis. While taking ""Corporate Practice R&D"" course, students are not allowed to take other courses, except those for ""Doctoral Thesis"", ""Special Topics Discussions"", and additional professional courses required for the Doctoral candidacy qualification review.
4. The minimum credits per Semester is 1 credit.
5. ""Doctoral Thesis"" is a mandatory 12 credits, awarded once after passing the oral examination.
6. The listed elective courses are for reference only and may be adjusted based on actual situation.
7. This form created on 2026/03/10.

半導體科技博士學位學程
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